DIGITAL SIGNATURE SYSTEM AND METHOD BASED ON
HARD LATTICE PROBLEM

BACKGROUND OF THE INVENTION

1.   Field of the Invention

The present invention relates generally to systems and methods for producing digital signatures based on the hardness of solving a worst-case lattice problem.

2.   Description of the Related Art

Digital signatures are used for many applications, including verifying the identity of the sender of a message. Most digital signature schemes rely on the difficulty of factoring a large number obtained as a product of two large prime numbers, or on computing discrete logarithms.

Goldreich et al. proposed using lattice reduction problems as a basis for producing digital signatures in Advances in Cryptography - CRYPTO, Springer LNCS, 1294:112-131 (1997). A lattice is a collection of points in n-dimensional space which satisfy certain properties, including (1) zero is in the set; (2) if a, b are in the set, then a+b, a-b are also in the set; (3) the lattice is generated by at least one finite basis, i.e., there exists a finite set (called a "basis") such that every point in the lattice is expressible as an integer linear combination of the elements in the basis. The "length" of a basis is the length of the longest vector in the basis. It happens that a

lattice typically can be defined using one of many bases, with the shortest basis being hard to find when the number "n" of dimensions becomes large.

Accordingly, the present invention recognizes that in a lattice-based digital signature scheme, an n-dimensional lattice can be generated that has a hard-to-find short basis, which is used as a sender's private key to sign a message by mapping the message to a point in the n-dimensional space. A recipient of the message can access a public key - the lattice with a relatively long basis - to verify the sender's identity by verifying the location of the message in the n-dimensional space. Unfortunately, the scheme disclosed by Goldreich et al., as admitted by Goldreich et al., might result in mapping two messages close together in the n-dimensional space, which would defeat the scheme as to those two messages because both messages would have the same digital signature.

In the present assignee's U.S. Patent No. 5,737,425 to Ajtai, incorporated herein by reference, an interactive message authentication system is disclosed which uses lattices. Although directed primarily to message authentication, the '425 patent discloses a method for deriving a lattice with a short basis. As recognized by the present invention, however, a digital signature system, unlike a message authentication system, must provide irrefutability of a signature, such that a recipient of a message can show a message to a third party to prove the identity of the signer of the message, a feature not generally required in message authentication systems. The requirement of irrefutability is particularly important in e-commerce applications. Moreover, the invention disclosed in Ajtai is interactive, which in the context of digital signatures

could render it susceptible to so-called "intruder in the middle" attacks. With the above recognitions in mind, the present invention has provided the inventive solutions disclosed below.

SUMMARY OF THE INVENTION

5   A computer-implemented method is disclosed for digitally signing data. The method includes generating a lattice $\mathcal{L}$ having at least one short basis establishing a private key and at least one long basis establishing a public key. Further, the method includes mapping at least the message μ or a concatenation thereof to a message point "x" in n-dimensional space using a function "f". The function "f" is selected such that 10 the possibility of mapping two messages close together in the space is infeasible. Using the short basis, a lattice point "y" of the lattice $\mathcal{L}$ is found that is close to the message point "x".

      In a preferred embodiment, at least the message point "x" and the lattice point "y" are returned as a digital signature. If desired, the function "f" can be randomized 15 by concatenating the message μ with a random number ρ. Both the message μ and random number ρ are binary strings.

      In one embodiment, the function "f" maps the message μ to a point on a grid. In this embodiment, the function "f" can be collision intractable, the collision intractability of which is derived from the hardness of lattice problems. In another 20 embodiment, the function "f" is collision intractable. In still another embodiment, the function "f" maps at least the message to a point on an auxiliary lattice.

The present method can also include verifying a digital signature at a receiver computer at least in part by determining whether a difference between the lattice point "y" and the message point "x" is no more than a predetermined distance. The predetermined distance can be related to the number of dimensions in the lattice $\mathcal{L}$.

In another aspect, a computer program storage device includes a program of instructions for generating a digital signature for a message. The program of instructions in turn includes computer readable code means for mapping a message μ or a concatenation with a random string ρ to a message point "x" in n-dimensional space, with the message point "x" being a point of a grid or a point of an auxiliary lattice. Also, computer readable code means find a point "y" of a key lattice $\mathcal{L}$ that is nearby the message point "x", and computer readable code means establish a digital signature, based at least on the points "x" and "y".

In still another aspect, a computer system for generating a digital signature of a message μ includes at least one sender computer. The sender computer includes logic for executing method steps that include mapping the message μ to a message point "x" at which it is not feasible to map any other message. Moreover, the logic of the sender computer finds a lattice point "y" that is relatively close to the message point "x", and then the logic transmits at least the message μ and the points "x" and "y". Further, the system includes at least one receiver computer that receives the message μ and points "x" and "y" and that executes logic including determining whether a distance between the points "x" and "y" is related in a predetermined way

to a predetermined distance. Based thereon, it is determined whether the message μ has been properly signed.

In yet another aspect, a computer-implemented method for digitally signing data includes generating a lattice $\mathcal{L}$ having at least one short basis and at least one long basis. The method also includes mapping at least the message μ or a concatenation thereof to a message point "x" in n-dimensional space. The message point "x" is an element of a set of spaced-apart points. Using the short basis, a lattice point "y" of the lattice $\mathcal{L}$ is found that is close to the message point "x".

The details of the present invention, both as to its structure and operation, can best be understood in reference to the accompanying drawings, in which like reference numerals refer to like parts, and in which:

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a block diagram of the present system;

Figure 2 is a flow chart of the logic used during generation of a lattice-based digital signature; and

Figure 3 is a flow chart of the logic for verifying the digital signature.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring initially to Figure 1, a preferably non-interactive system for generating digital signatures based on lattice problems is shown, generally designated 10. Because the preferred system 10 is non-interactive, it is immune from so-called

"intruder in the middle" attacks. In the particular architecture shown, the system 10 includes a sender computer 12 that executes a software-implemented digital signature module 14 in accordance with the logic below to digitally sign messages. As shown in Figure 1, the sender computer 12 can send a message μ, a message point "x", a lattice point "y", and, if desired, a randomly generated number ρ in accordance with the disclosure below to a receiver computer 16. In turn, the receiver computer 16 executes a receiver module 18 to verify the signature.

It is to be understood that the logic disclosed herein may be executed by a processor as a series of computer-executable instructions. The instructions may be contained on a data storage device with a computer readable medium, such as a computer diskette. Or, the instructions may be stored on a DASD array, magnetic tape, conventional hard disk drive, electronic read-only memory, optical storage device, or other appropriate data storage device. In an illustrative embodiment of the invention, the computer-executable instructions may be lines of compiled C^{++} compatible code.

In any case, the flow charts herein illustrate the structure of the modules of the present invention as embodied in computer program software. Those skilled in the art will appreciate that the flow charts illustrate the structures of computer program code elements including logic circuits on an integrated circuit, that function according to this invention. Manifestly, the invention is practiced in its essential embodiment by a machine component that renders the program code elements in a form that

instructs a digital processing apparatus (that is, a computer) to perform a sequence of function steps corresponding to those shown.

Figure 2 shows the logic of the digital signature module 14. The following notation is used for the below discussion. The notation that $x \in_R X$ means that the number "x" is chosen uniformly randomly from the set X. For a binary string x, the symbol $|x|$ denotes its length. For binary strings x, y, the symbol $x \circ y$ denotes their concatenation. All distances and norms are assumed to be Euclidean. For all integers a, b >0, the notation Z^a_c is the set of all a-tuples of integers in the set $\{0,1,...,c-1\}$. Similarly, $Z^{a \times b}_c$ is the set of all matrices of "a" rows and "b" columns whose elements are integers in the set $\{0,1,...,c-1\}$. For a set $b_1,...b_n$ of vectors, $\mathcal{L}$ denotes the lattice of all integer linear combinations of $b_1,...b_n$, with the vectors establishing a "basis" of the lattice $\mathcal{L}$. The length of a basis is the Euclidean norm of the length of the longest vector in the basis. Finally, the symbol [x] denotes the integer portion of a number x.

With the above discussion in mind, commencing at block 20 in Figure 2, a lattice $\mathcal{L}$ is generated that has a short basis and at least one long basis. The lattice $\mathcal{L}$ preferably is generated using the principles set forth in the present assignee's U.S. Patent No. 5,737,425 to Ajtai. It is to be understood that the short basis of the lattice $\mathcal{L}$ is generated along with the lattice, but that once the lattice is known, it is a difficult if not impossible problem to reverse engineer the short basis. The long basis of the lattice, accordingly, is published as the public key at block 22 and the short basis is maintained in secrecy as the private key of the present digital signature scheme.

In any case, in the preferred method for generating the lattice $\mathcal{L}$, set forth in the above-referenced patent, a variable "r" is selected that is sufficiently large such that the worst-case problems discussed in Ajtai, "Generating Hard Instances of Lattice Problems", Proc. 28th ACM Symposium on Theory of Computing, pages 99-108 (1996) and incorporated herein by reference, are hard. Moreover, variables c_{L1} and c_{L2} are selected such that it is infeasible to find vectors of length r^3 in n-dimensional lattices constructed in accordance with the above-referenced patent. Preferably, $c_{L2} \geq 9$ and $c_{L1} > c_{L2}$.

Letting $n = c_{L1}r(\log r)$, finding vectors of length r^3 is infeasible in the n-dimensional lattice $\mathcal{L}$ that is created at block 20, assuming that certain worst-case lattice problems are hard in lattices of dimension n. Further, let q_L be the least odd integer satisfying $q_L \geq [r^{c_{L2}}]$, let $K = r^3$, and let $M = (nq_L)^{\frac{1}{2}}$. The preferred key lattice $\mathcal{L}$ is a random lattice in $\Gamma'(n,M)$ as defined in the above-referenced patent, where an efficient construction of the lattice is also described that has a short basis generated along with it having a length of at most K/3n. In contrast, the public (long) basis preferably is at most of length M.

When the sender computer 12 desires to send a message μ, it enters a DO loop at block 24. Moving to block 26, the logic can, if desired, concatenate the message μ with a random string ρ. Then, proceeding to block 28, the message μ (or, more preferably, the concatenation $\mu \circ \rho$) is mapped to a message point "x" in n-dimensional space using a function "f" that is chosen such that it is infeasible that two messages would be mapped close to each other in space. "Close" is defined further

below in the context of the two grid-based mapping methods and one auxiliary lattice-based mapping method.

More specifically, for the grid-based methods, assume that A is an n-dimensional grid of size "d", where the preferred $d = r^4$. Also, let $\ell = n^3 q_L$, and let the above-mentioned function "f" be established by a mapping hash function H: $\{0,1\}^n \rightarrow \{0,1\}^\ell$. Further assume that the magnitude of the message is one-half n, i.e., that $|\mu| = n/2$. First, ρ is selected from the set $\{0,1\}^{n/2}$, and then the message point "x" is determined as an n-tuple of integers multiplied by "d" as follows: $x = H(\mu \circ \rho)d$. If the message point "x" as computed happens to be a point on the key lattice $\mathcal{L}$, the process above repeats with a new random string ρ.

In a first implementation of the grid-based method, the mapping function H is any hash function that satisfies the so-called Magic Hash Function condition that there exists an efficiently and publicly computable function that behaves like a random oracle. Some combination of hash functions such as Message Digest 5 (MD-5), "Sha", and "Snefru" are assumed to approximate the Magic Hash Function. Such a function is not collision-intractable.

In a second implementation of the grid-based method, the mapping function H is a collision-intractable function, preferably a lattice-based hash function, wherein c_{L1} and c_{L2} have the property that it is infeasible to find vectors of length r^3 in the lattice described in the above-referenced Ajtai publication. In this implementation, assume that q_H is the least odd integer satisfying $q_H \geq [r^{cL2-4}]$. The output of the hash

function is n-tuples of integers in the set $\{0,1,...,q_{H-1}\}$. Further assume that $c_{H2} = c_{L2} +4$, and $c_{H1} = c_{L1}$, so that $n = c_{H1}r\log(r) = c_{L1}r\log(r)$.

With the above definitions in mind, in the grid-based collision intractable embodiment, the mapping function $H \in_R Z^{r \times n}{}_{qH}$, and a variable m is a vector in Z^n_2 that is an element of $\{0,1\}^n$. With this notation, $H(m) = Hm \bmod q_H \in Z^r_{qH}$, the output of which function is an r-tuple of integers in Z_{qH}. This output is interpreted as n integers of equal length, i.e., as a point in Z^n. As understood herein, it is computationally infeasible to find vectors of length "n" in the n-dimensional lattice of vectors $= x \in \{0,1\}^n$ such that $Hx = 0 \bmod q_H$. In other words, finding vectors of length $r^3 > n$ in the lattice of vectors defined by $x \in \{0,1\}^n$ is computationally infeasible. Moreover, it is to be appreciated that the collision intractability of the function "f" as implemented in the last of the above-disclosed grid-based mapping methods, and in the below-disclosed auxiliary lattice mapping method, is derived from the hardness of lattice problems.

As mentioned above, instead of using either of the two grid-based methods set forth above, a mapping using an auxiliary lattice can be undertaken at block 28. In this embodiment, assume that "A" is an n-dimensional auxiliary lattice chosen according to the same distribution as the key lattice $\mathcal{L}$ is chosen. Accordingly, $c_{A1} = c_{L1}$ and $n = c_{A1}r\log(r)$, $q_A = q_H$, and it is easy to find a basis for the auxiliary lattice A of length $M = (nq_A)^{1/2}$. Let P be a public matrix whose columns are the above-disclosed long basis vectors for the auxiliary lattice A.

With the above definitions in mind, the message μ is concatenated, if desired, with the random string ρ as before at block 26, but then at block 28 the message point "x" is determined by multiplying the concatenation by the public matrix P. If the message point x is found to be an element of the key lattice $\mathcal{L}$, another random string

5 ρ is selected and the process repeats.

In any case, it is to be appreciated that in the grid-based or lattice-based mapping schemes disclosed above, the message μ is mapped to a message point "x" that is a point on a grid or a lattice. In other words, in contrast to previous lattice mapping schemes the message point "x" must be an element of a set of points that are

10 spaced apart from each other in n-dimensional space, such that no two points in the set are close together. This makes it infeasible that any two messages will be mapped to locations that are sufficiently close together so as to make a single signature apply to both.

Once the message has been mapped to the message point, the logic moves

15 from block 28 to block 30, wherein a closest point "y" of the key lattice $\mathcal{L}$ to the message point "x" is determined, using the (private) short basis of the key lattice $\mathcal{L}$. Specifically, using the short basis, a point y ∈ $\mathcal{L}$ is obtained such that | | x-y | | ≤ nK/(3n) (which, it will be recalled, = $r^3/3$) by writing x as a linear (possibly non-integral) combination of vectors in the short basis, each of which has a length of at

20 most K/(3n), and then rounding the coefficients to get y ∈ $\mathcal{L}$. Then, at block 32, the message μ, random string ρ (if used), message point "x", and closest lattice point "y"

are output for transmission of the message with lattice-based digital signature to the receiver computer 16.

Figure 3 shows that logic by which the receiver module 18 of the receiver computer 16 verifies the signature output at block 32. Commencing at block 34, the message μ, random string ρ (if used), message point "x", and closest lattice point "y" are received. Moving to block 36, it is verified, using the long basis, that the lattice point "y" is indeed a point on the key lattice $\mathcal{L}$. If desired, it can be further verified that $\mu \circ \rho \in Z^n_2$. When a grid-based mapping method is employed, it can be further verified that $x = H(\mu \circ \rho)d$, whereas when an auxiliary lattice mapping method is used it can be verified that $x = P(\mu \circ \rho)$.

Moreover, using the long basis the receiver computer 16 moves to block 38 to verify that the message point "x" is indeed close to the lattice point "y". In a particularly preferred embodiment, this is done by verifying that $||x\text{-}y|| \leq r^3/3$. More generally, at block 38 it is determined whether a difference between the lattice point "y" and the message point "x" is no more than a predetermined distance. If any test fails, it can be determined that the message μ has not been properly signed.

While the particular DIGITAL SIGNATURE SYSTEM AND METHOD BASED ON HARD LATTICE PROBLEM as herein shown and described in detail is fully capable of attaining the above-described objects of the invention, it is to be understood that it is the presently preferred embodiment of the present invention and is thus representative of the subject matter which is broadly contemplated by the present invention, that the scope of the present invention fully encompasses other

embodiments which may become obvious to those skilled in the art, and that the scope of the present invention is accordingly to be limited by nothing other than the appended claims, in which reference to an element in the singular is not intended to mean "one and only one" unless explicitly so stated, but rather "one or more". All structural and functional equivalents to the elements of the above-described preferred embodiment that are known or later come to be known to those of ordinary skill in the art are expressly incorporated herein by reference and are intended to be encompassed by the present claims. Moreover, it is not necessary for a device or method to address each and every problem sought to be solved by the present invention, for it to be encompassed by the present claims. Furthermore, no element, component, or method step in the present disclosure is intended to be dedicated to the public regardless of whether the element, component, or method step is explicitly recited in the claims. No claim element herein is to be construed under the provisions of 35 U.S.C. §112, sixth paragraph, unless the element is expressly recited using the phrase "means for".

WE CLAIM: